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*Lopez, M.A.; Mehta, D.P.;*

VLSI, 1996. Proceedings., Sixth Great Lakes Symposium on , 22-23 March 1996

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### 2 Phased array shaped multi-beam optimization for LEO satellite communications using a genetic algorithm

*Sherman, K.N.;*

Phased Array Systems and Technology, 2000.

Proceedings. 2000 IEEE International Conference on , 21-25 May 2000

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### 3 A methodology for converting polygon based standard cell from bulk CMOS to SOI

*Wu, K.Y.; Chan, P.C.H.;*

Electron Devices Meeting, 1994. Proceedings., 1994 IEEE Hong Kong , 18 July 1994

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[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) **IEEE CNF**

**4 Smallest paths in simple rectilinear polygons***McDonald, K.M.; Peters, J.G.;*

Computer-Aided Design of Integrated Circuits and  
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[\[Abstract\]](#) [\[PDF Full-Text \(1108 KB\)\]](#) **IEEE JNL**

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